

Configuration Manual

Industrial Internship
MSc Cybersecurity

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National College of Ireland

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National College of Ireland
MSc Project Submission Sheet
School of Computing



Student

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Programme: MSc Cybersecurity

Year: 2023

Module: Industrial Internship

Lecturer: Jawad Salahuddin

Submission

Due Date: 05-01-2024

Project Title: Enhancing LoRaWAN security: Authentication strategies to mitigate rogue device infiltration

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Signature: Adil Mustafa Khokhawala

Date: 04-01-2024

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Configuration Manual

Adil Mustafa Khokhawala
Student ID: 22144188

1 Introduction:

This document has been prepared to discuss the steps involved in the implementation of the conducted research. In this project, we have created a Lora network prototype in MATLAB and demonstrated an authentication method for preventing rogue devices from penetrating the network. Additionally, a pilot simulation has been done for the signal parameters of 868 MHz and channel bandwidth of 125 KHz has been shown. To simulate the Poc of the solution different Simulink libraries, Channel blocks and C programming language were used for encryption. This configuration manual gives a detailed synopsis to set up the simulation environment and execute the project successfully.

2 Simulation Set up:

Step 1: The first step is to download and install the simulation environment. We have used MATLAB version *R2023a* for this research. The MATLAB version can be downloaded using this link: <https://matlab.mathworks.com/>

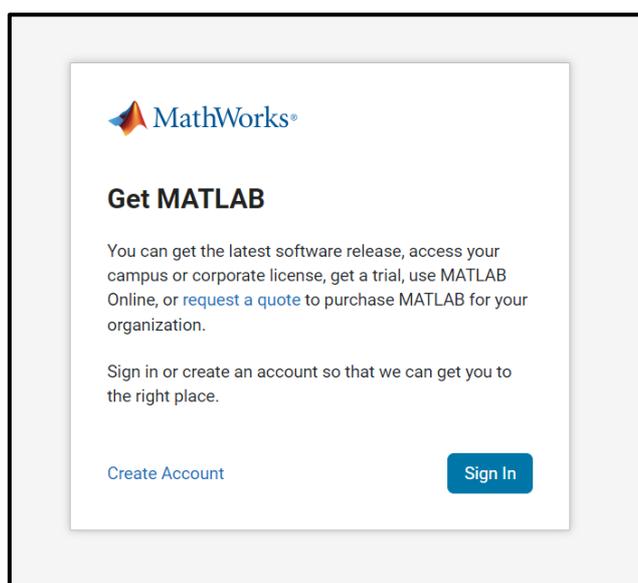


Figure 1: Sign In

Step 2: After completing the account creation, and signing in process, we need to download the desktop client for MATLAB. The installer can be downloaded by clicking on the button for the R2023a version.

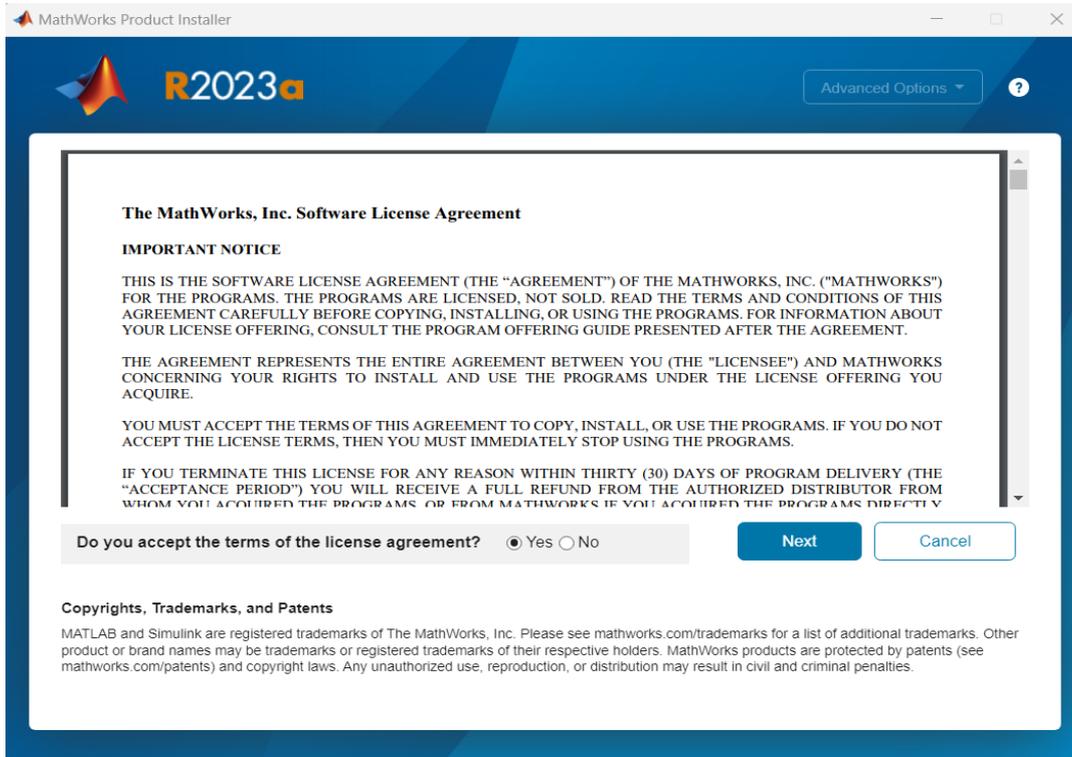


Figure 2: License Agreement

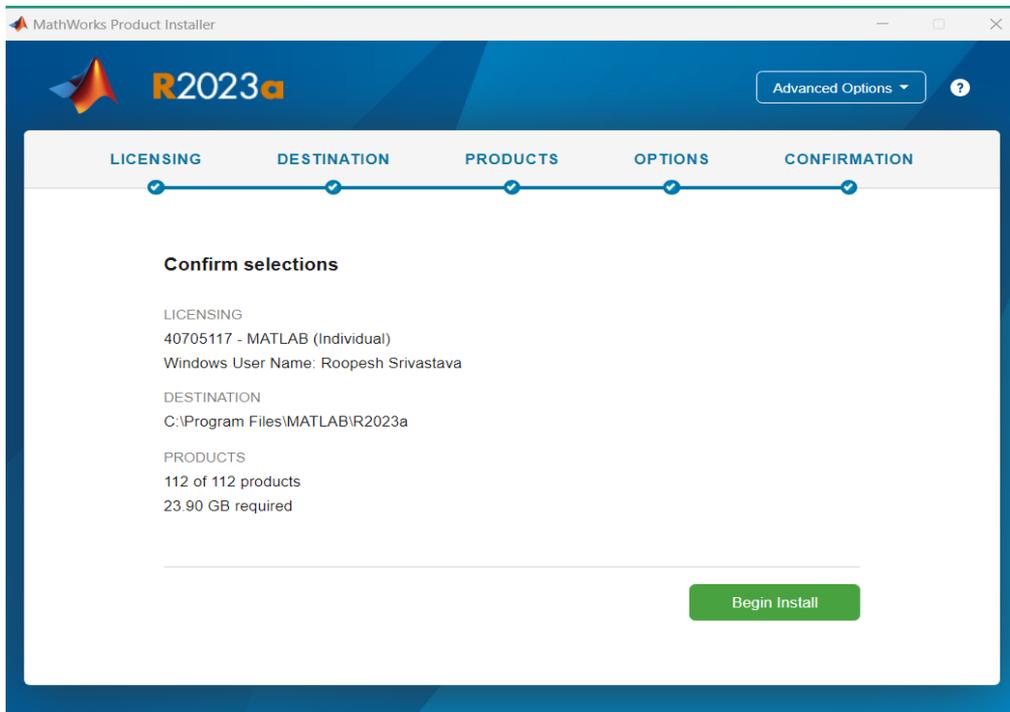


Figure 3: Begin Installation

Step 3: Upon installing MATLAB, we select all the default options and install all the Simulink libraries in the list. This will begin the installation.

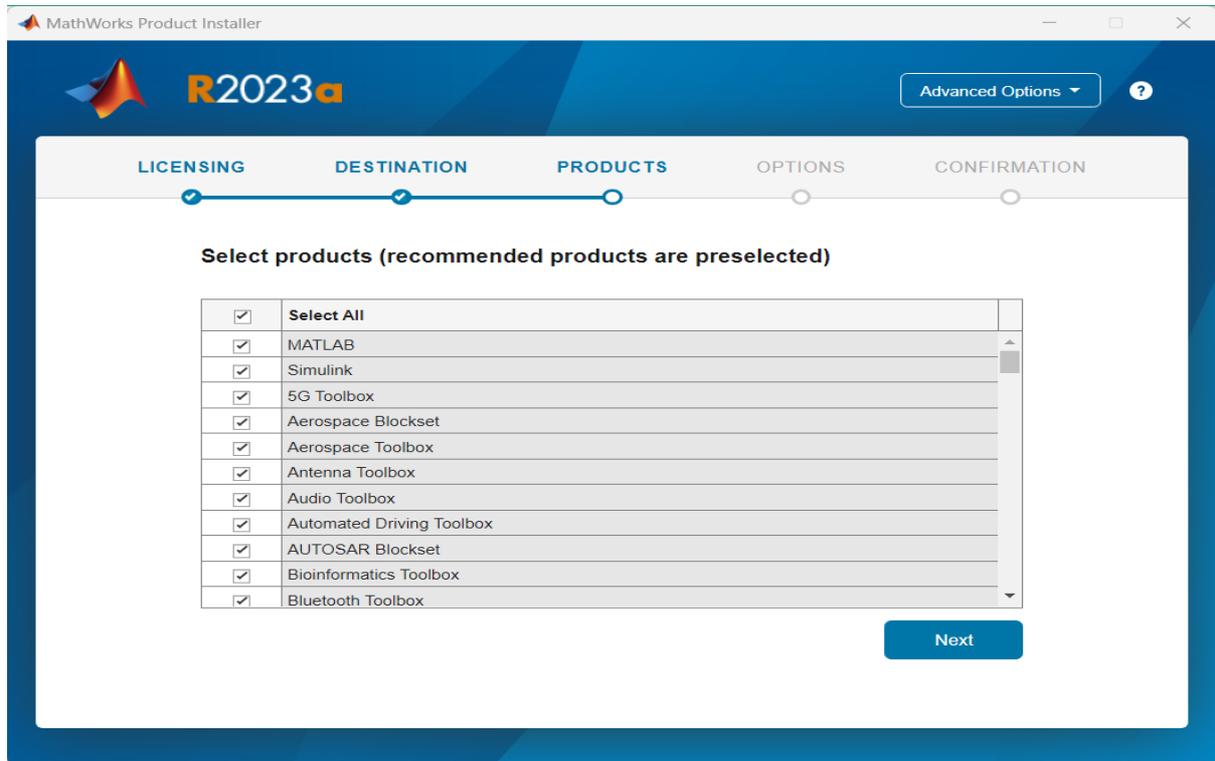


Figure 4 : Product List

Step 4: Launch the MATLAB R2023a client and boot up the software for simulation.

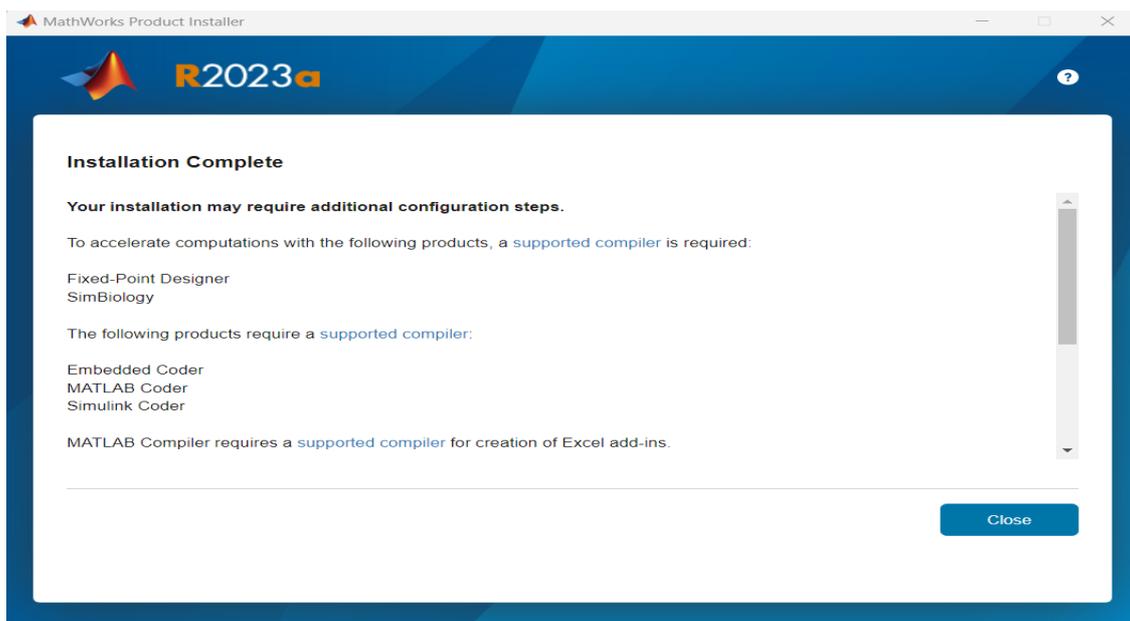


Figure 5: Installation Complete

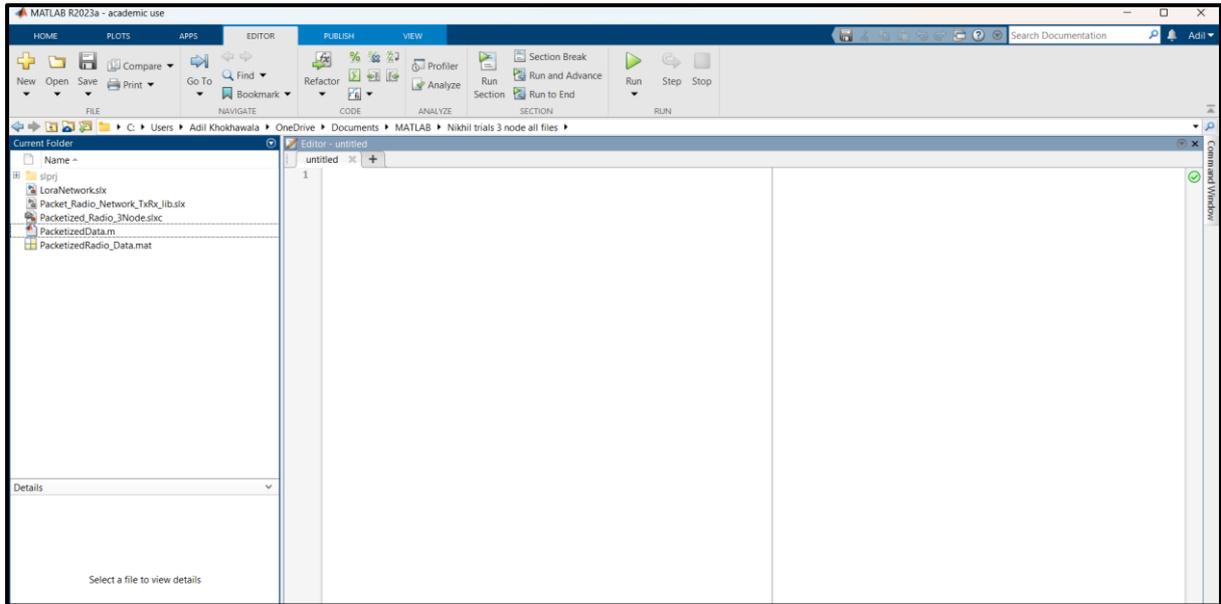


Figure 6: MATLAB boot-up interface

3 Running the simulation:

Now that the simulation environment is set up, we run the files in a specific order so that the Simulink files have received the node signals, which are running and can generate network traffic.

Step 1: The zip file needs to be extracted and copied to the MATLAB folder path. By doing this, we can then add the code files to the path for execution.

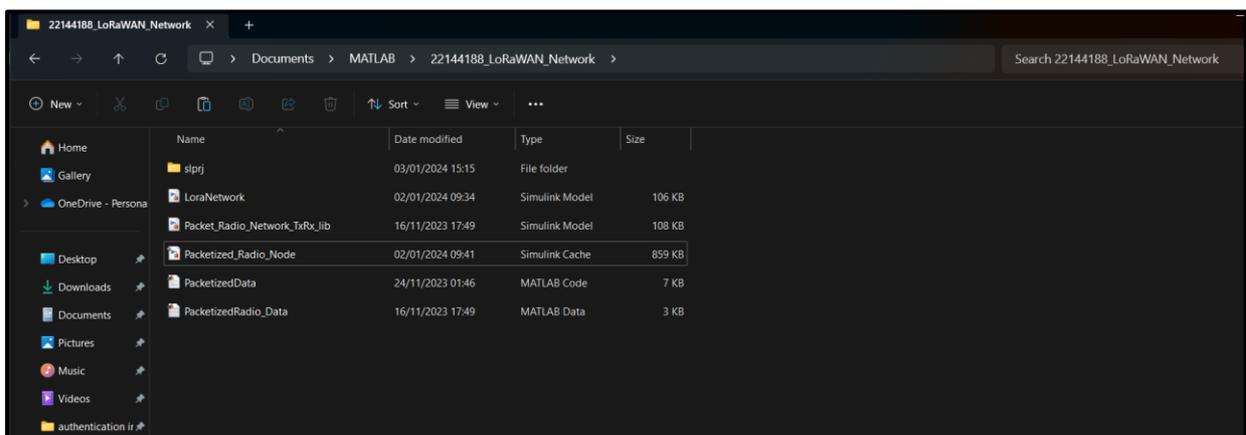


Figure 7: Files saved in the correct path

Step 2: For generating the LoRa Beacon signals, the files Lora Tx and Lora Rx are run. These are the files which contain the parameters inside the transmitter and receiver. After which, the Pilot.m file is run, to generate these beacon signals. They are primarily based on factors like spreading factor, bandwidth, power, frequency channel etc. Once this is run, a payload is created which uses the message fed as input. The IQ signal is passed on to the Lora receiver file with the other parameters to retrieve the message sent by the transmitter.

This process showcases the signal processing method in the LoRaWAN protocol in accordance with the LoRa Alliance.

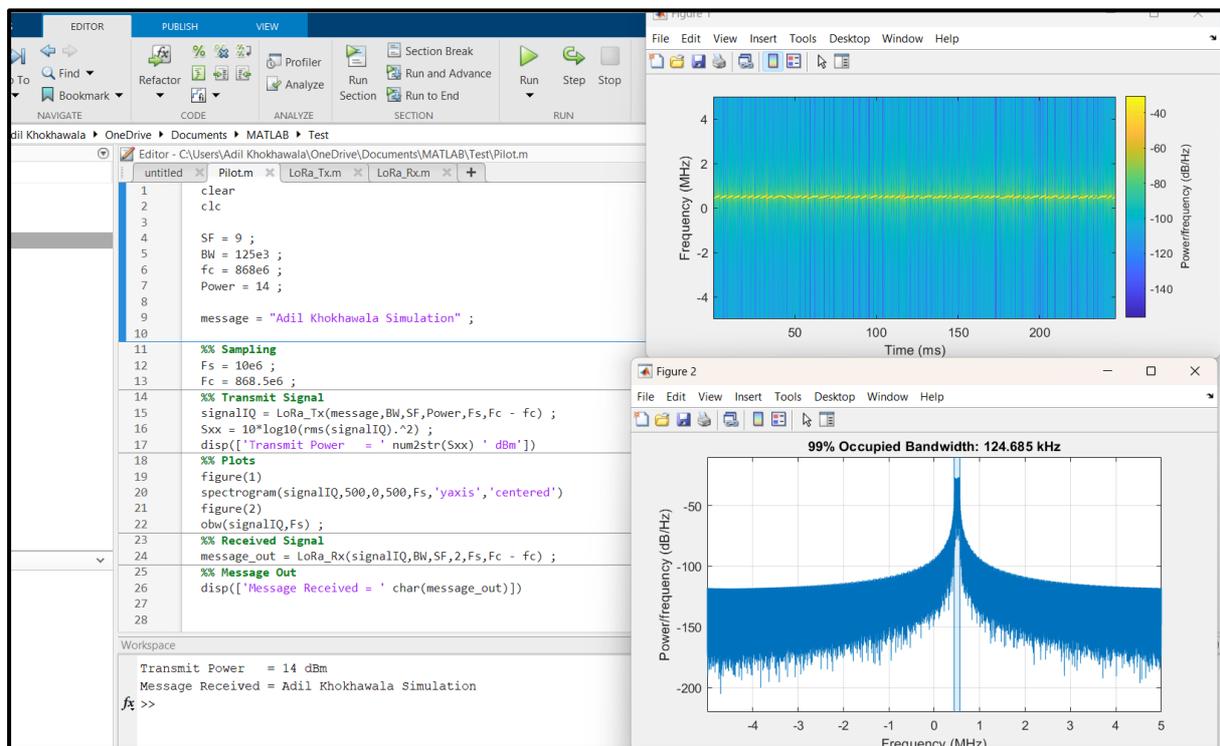


Figure 8: LoRa Beacon Simulation

Factors selected :

- ✓ Spreading Factor (SF) : 9
- ✓ Bandwidth (BW) : 125 KHz
- ✓ Centre Frequency: 868 MHz
- ✓ Power: 14 dBm
- ✓ Message : “Adil Khokhawala Simulation”

Step 3: To run the Simulink model for data packet processing, we need to run the Node frame file labelled (*PacketizedData.m*). Upon running this file, the requirements for 10 nodes are sent to the Simulink model where it is being called.

The ‘*Packetizeddata.m*’ file contains the transmitter and receiver codes where the nodes relate to a node 11, i.e. a gateway.

```

16 clear
17
18
19 % Load AGC and SYNC symbols. They are taken from data sequence "AGC_SYNC_Data"
20 load AGC_SYNC_Data; % Load AGC bits, SYNC bits, and st, the QPSK symbol string for DFE training. The training string
21 % is the QPSK symbols of the bit string of AGC and
22 % SYNC. The modulation rate is 2 bits per symbol.
23
24 % Generate Data Frame TxData for all 11 Ndoes (i=1,2,3)
25 for i=1:11
26
27 TxData.Agc=ppdu_bb(1:50,1); %Load AGC signal, 2 bits per symbol
28 TxData.Sync=ppdu_bb(51:304); %Load Sync Signal, 2 bits per symbol
29
30 N=(130)*8;
31 PyldLengthD=4+2+2+8+8+8+32+N+32; % PayloadBitsSize= 4+2+2+8+8+8+32+N+32 = 1136 @N=130*8.
32 PyldLength=decimalToBinaryVector(PyldLengthD, 16); % Convert a decimal number to a binary vector
33
34 % Add CRC Checksum Using Matlab CRC Generator
35 crcGen=comm.CRCGenerator();
36 x=logical(PyldLength'); %CRC-16 with 16 Checksum
37 plcrc=crcGen(x);
38 TxData.PayloadLength=PyldLength'; %Load Payload Length
39 TxData.HeaderCrc=plcrc(17:32,1); %Load Header CRC
40
41 % Generate the MAC Header
42 % | Type 4b | Version 2b | Reserved 2b | To Add 8b | From Add 8b |
43 % Sequence No. 8b | Time Stamp 32b |

```

Figure 9: Creating frame for packets

```

96 rng(11);
97 TxData.Payload=randi([0 1], [N 1]);
98 TxData.Payload(length(TxData.Payload),1)=0;
99 PyldBits=[ TxData.Type; ...
100 TxData.Version; ...
101 TxData.Reserved; ...
102 TxData.ToAddress; ...
103 TxData.FromAddress; ...
104 TxData.SequenceNo; ...
105 TxData.Time; ...
106 TxData.Payload];
107
108 crcGen=comm.CRCGenerator('z^32+z^26+z^23+z^22+z^16+z^12+z^11+z^10+z^8+z^7+z^5+z^4+z^2+z^1+1');
109 reset(crcGen);
110
111 PyldCRC=crcGen(PyldBits);
112
113 % Final TxData Frame is
114 TxData.PayloadCRC=PyldCRC(length(PyldCRC)-31:length(PyldCRC),1);
115 TxSignalBits= [TxData.Agc; ...
116 TxData.Sync; ...
117 TxData.PayloadLength; ...
118 TxData.HeaderCrc; ...
119 TxData.Type; ...
120 TxData.Version; ...
121 TxData.Reserved; ...
122 TxData.ToAddress; ...
123 TxData.FromAddress; ...
124 TxData.SequenceNo; ...
125 TxData.Time; ...
126 TxData.Payload; ...
127 TxData.PayloadCRC];
128
129 if i==1
130 TxDataBitsN1=TxSignalBits; % Rename the Data Frame for Node1

```

Figure 10: Tx_Data Frame Creation

```

Editor - PacketizedData10Node.m
PacketizedData10Node.m
197 TxAck.ToAddress = [0 0 0 1 0 0 1]; % This is Address of the receiving Node9
198 TxAck.FromAddress = [0 0 0 1 0 1 1]; % This is Address of the sending Node11
199
200 elseif i==10
201 TxAck.ToAddress = [0 0 0 1 0 1 0]; % This is Address of the receiving Node10
202 TxAck.FromAddress = [0 0 0 1 0 1 1]; % This is Address of the sending Node11
203
204 end
205
206 TxAck.SequenceNo = TxData.SequenceNo;
207
208 AckBits=[TxAck.Type; ...
209 TxAck.Version; ...
210 TxAck.Reserved; ...
211 TxAck.ToAddress; ...
212 TxAck.FromAddress; ...
213 TxAck.SequenceNo];
214
215 reset(crcGen);
216 AckCRC=crcGen(AckBits);
217
218 TxAck.AckCrcBits=AckCRC(length(AckCRC)-31:length(AckCRC),1);
219
220 TxAckBits= [TxAck.Abc; ...
221 TxAck.Sync; ...
222 TxAck.PayloadLength; ...
223 TxAck.HeadCrcBits; ...
224 TxAck.Type; ...
225 TxAck.Version; ...
226 TxAck.Reserved; ...
227 TxAck.ToAddress; ...
228 TxAck.FromAddress; ...
229 TxAck.SequenceNo; ...
230 TxAck.AckCrcBits];
231
232 if i==1
233 TxAckBits1=TxAckBits; % Rename the ACK Frame for Node1
234 elseif i==2
235 TxAckBits2=TxAckBits; % Rename the ACK Frame for Node2
236 elseif i==3
237 TxAckBits3=TxAckBits; % Rename the ACK Frame for Node3
238

```

Figure 11: Tx_Ack Frame Creation

Step 4: We must now run the MATLAB data file labelled “*Packetized+Data.mat*”, this file defines all the variables present in the code. Factors like Ts, MDSN, Node definition, bandwidth etc. are defined. These can be run or either loaded from the “.mat” file.

Name	Value
st	500x1 complex d...
tout	600001x1 double
Ts	1.0000e-07
TxAckBitsN1	400x1 double
TxAckBitsN2	400x1 double
TxAckBitsN3	400x1 double
TxDataBitsN1	1472x1 double
TxDataBitsN2	1472x1 double
TxDataBitsN3	1472x1 double

Figure 12: MATLAB data file

Step 4: Next step is to run the Simulink file *LoraNetwork.slx*. This runs the created Simulink model in which the network is defined and connected to the gateway in a star topology. Additionally, each node is defined with a transmitter and receiver through Rayleigh SISO channels and AWGN Channel to generate signal. We have used these 2 channels as an alternate because MATLAB does not support lora blocks in Simulink.

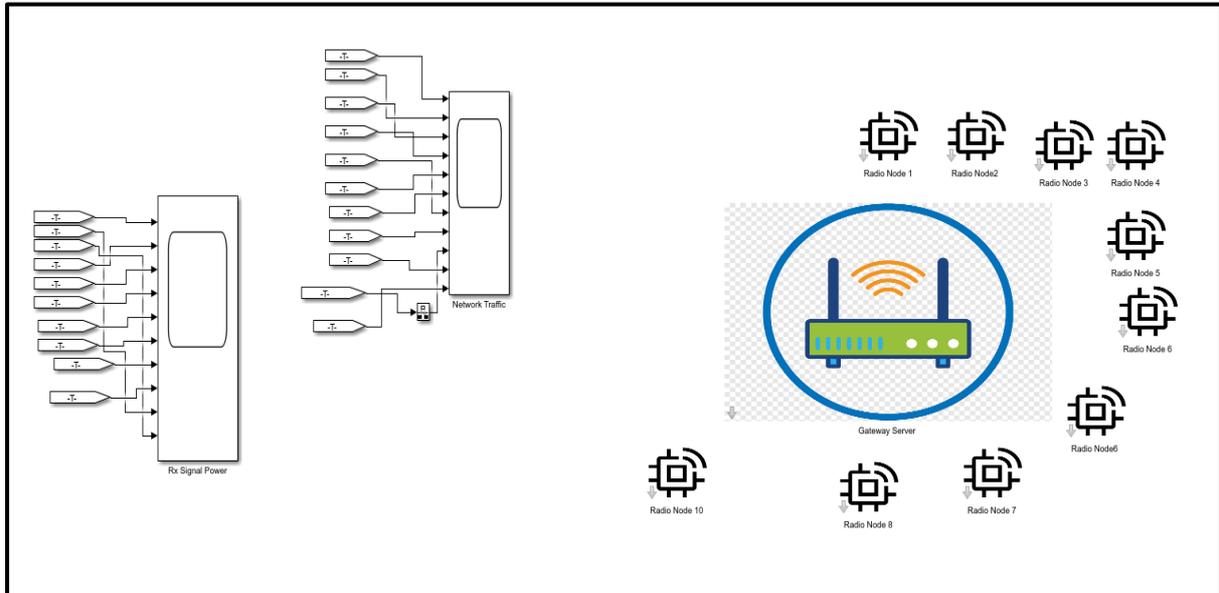


Figure 13: LoRa Network with 10 Nodes

Once the files are run in the order mentioned above the Lora network starts to produce network traffic, i.e. a bidirectional communication takes place between the sensor nodes and the gateway server.

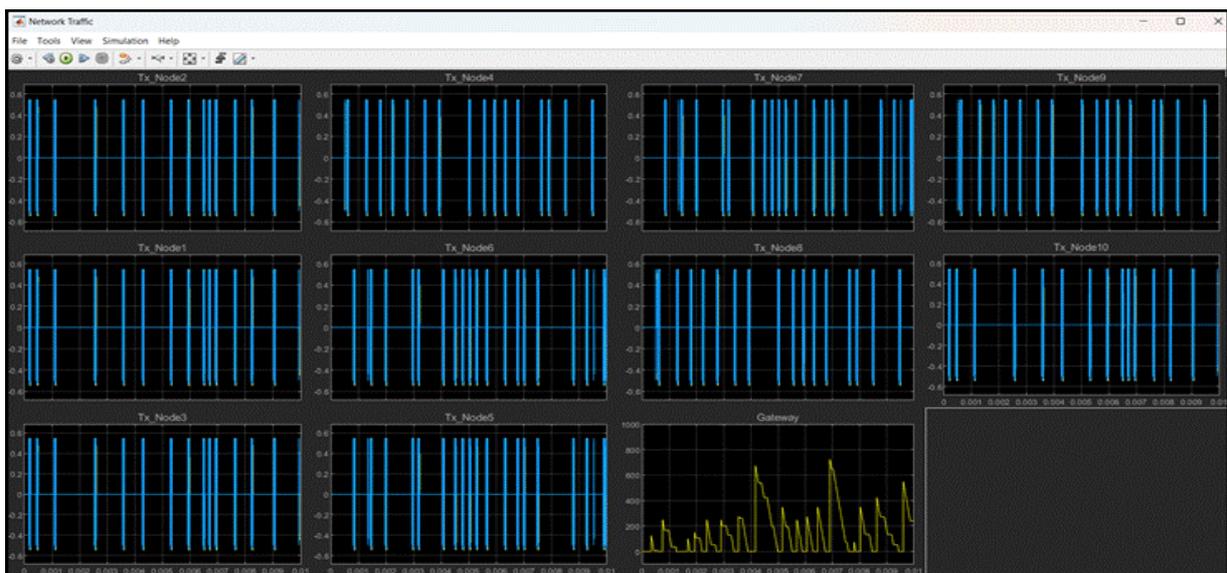


Figure 14: Transmitted Signals

The below image shows the received power at the sensor node end.

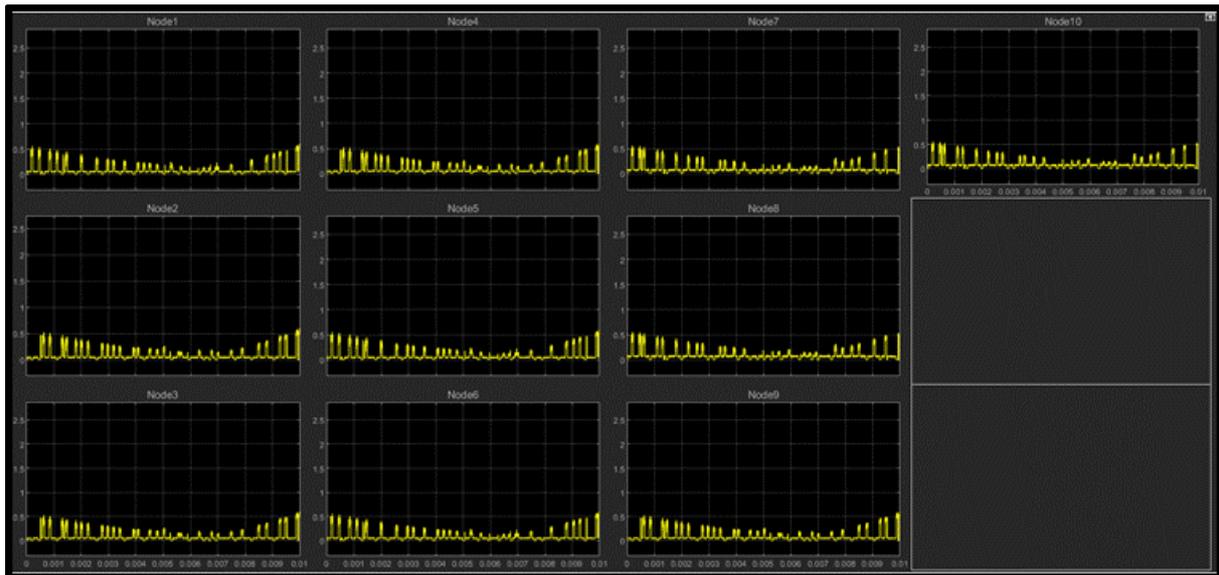


Figure 15: Received Signal Power (Rx)

4 Poc Implementation

The Poc of the proposed solution was implemented by the company’s research team. The code developed for the time-synchronized random seed generator and the code for checking the authenticity at the gateway end were provided to the company. They flashed the code onto the ESP32 Chip and the Pragsate LoRa node. This is a general-purpose chip which is placed before the gateway server. This research does not alter any rule/standard in the LoRa protocol; it only adds an additional layer of security to increase the complexity and time penalty for any attacker that is trying to impersonate a compliant node in the network. Our solution checks the parameter and the random seed value at both ends and matches the timestamp. If the timestamp is less than 5 minutes, it allows the packet to pass through to the application server, else it discards the packet and throws a message stating, **“Rogue Device, Invalid Packet!”**.

Below is an example of how the random seed value is appended to the authentication packet, which increase the complexity of the entire data transmission process and makes it difficult for a threat actor to intercept the packet and disrupt the network.

<nonce value>	b'53027dz12c5oJzqRLMrRU0mRz3128930dd3f52e3ea'
----------------------------	---

Figure 6 : Authentication packet

91	b'53027dz12c5oJzqRLMrRU0mRz3128930dd3f52e3ea'
-----------	---

Figure 7: Appended packet sent for further transmission.

Below attached are Poc code snippets provided to the company's research team for flashing onto the ESP32 Chip and the Pragsate Lora Node to implement the time-synchronous random seed generator. The code was modified at the company's end for implementing it on the hardware level.

```
#include <stdio.h>
#include <stdlib.h>
#include <time.h>

int generate_random_number() {
    return rand() % 100 + 1;
}

void append_random_number(char *message) {
    int random_number = generate_random_number();
    sprintf(message + strlen(message), " %d", random_number);
}

void append_timestamp(char *message) {
    time_t rawtime;
    struct tm *timeinfo;

    time(&rawtime);
    timeinfo = localtime(&rawtime);

    char buffer[20];
    strftime(buffer, sizeof(buffer), "%Y-%m-%d %H:%M:%S", timeinfo);
    strcat(message, buffer);
}

int main() {
    char input_message[1000];
    printf("Enter your message: ");
    fgets(input_message, sizeof(input_message), stdin);
    input_message[strlen(input_message)] = '\0';

    char composite_output[1000];
    strcpy(composite_output, input_message);
    append_random_number(composite_output);
    append_timestamp(composite_output);
    printf("%s\n", composite_output);

    return 0;
}
```

Figure 16: Random seed appended to data packet

```

#include <stdio.h>
#include <stdlib.h>
#include <time.h>
#include <string.h>

int generate_random_number() {
    return rand() % 100 + 1;
}

void append_random_number(char *message) {
    int random_number = generate_random_number();
    sprintf(message + strlen(message), " %d", random_number);
}

void append_timestamp(char *message) {
    time_t rawtime;
    struct tm *timeinfo;

    time(&rawtime);
    timeinfo = localtime(&rawtime);

    char buffer[20];
    strftime(buffer, sizeof(buffer), "%Y-%m-%d %H:%M:%S", timeinfo);
    strcat(message, buffer);
}

int is_valid_message(char *composite_output) {
    int random_number_recovered, random_number_generated;
    char timestamp[20];

    sscanf(composite_output, "%*[^0-9]%d", &random_number_recovered);
    strcpy(timestamp, strchr(composite_output, ' ') + 1);

    random_number_generated = generate_random_number();

    if (random_number_recovered != random_number_generated) {
        return 0;
    }

    time_t current_time, message_time;
    struct tm tm;

```

Figure 17: Gateway checking the authenticity

```

time(&current_time);
sscanf(timestamp, "%d-%d-%d %d:%d:%d",
        &tm.tm_year, &tm.tm_mon, &tm.tm_mday,
        &tm.tm_hour, &tm.tm_min, &tm.tm_sec);
tm.tm_isdst = -1;
message_time = mktime(&tm);

long diff = current_time - message_time;
if (diff < 0) {
    diff = -diff;
}

return diff < 5 * 60; // if the difference between timestamp is less than 5 mins.
}

int main() {
    char input_message[1000];
    printf("Enter your message: ");
    fgets(input_message, sizeof(input_message), stdin);
    input_message[strlen(input_message)] = '\0';

    char composite_output[1000];
    strcpy(composite_output, input_message);
    append_random_number(composite_output);
    append_timestamp(composite_output);

    if (is_valid_message(composite_output)) {
        printf("Valid packet: %s\n", composite_output);
    } else {
        printf("Rogue Device, Invalid packet: %s\n", composite_output);
    }

    return 0;
}

```

Figure 18: Gateway checking the authenticity

5 Conclusion

The configuration manual contains the detailed steps to execute the implementation process of the research project in a sequential format and makes it easy for any user to understand the flow of the model.

6 Monthly Internship Activity Report

The internship commenced on 25th September 2023 to 10th December 2023.

Monthly Internship Activity Report

The Internship Activity Report is a 1-page monthly summary of the activities performed by you and what you have learned during that month. The Internship Activity Report must be signed off by your Company and included in the configuration manual as part of the portfolio submission.

Student Name: **Adil Mustafa Khokhawala**

Student number: **22144188**

Company: **Tynatech Ingeneous Pvt. Ltd.**

Month Commencing: **October 2023**

1. Introduction to company catalog
2. Introductory interaction with Solution team for
 - a. Water Management solution.
 - b. Tynarace Solutions
 - c. Health Monitoring and Energy monitoring
3. Introduction to LoRaWAN technology
4. Research Work
 - a. Create a deck to summarize two papers on vulnerabilities in LoRaWAN
Create a deck to summarize the security vulnerabilities in LoRaWAN network.
 - b. Create a deck for authentication methods, security issues caused due to lack of authentication in LoRaWAN.
5. Work with the ticketing team and engage in troubleshooting calls.

Employer comments

Adil puts a lot of effort into his work. He has demonstrated a good grasp of topics and has a positive attitude while learning and approaching tasks. He has been engaging very well in his day-to-day activities and in his research topic as well.

Student Signature:  Date: 01/11/2023

Industry Supervisor Signature:  Date: 01/11/2023

Monthly Internship Activity Report

The Internship Activity Report is a 1-page monthly summary of the activities performed by you and what you have learned during that month. The Internship Activity Report must be signed off by your Company and included in the configuration manual as part of the portfolio submission.

Student Name: **Adil Mustafa Khokhawala**

Student number: **22144188**

Company: **Tynatech Ingeneous Pvt. Ltd.**

Month Commencing: **November 2023**

1. Configuration of LoRaWAN Chip, Chip no. PRASGATE LORA node with LoRa Chip SX1276 & SX1278.
2. Hands-on configuration for the current solutions implemented in the company.
 - a. Water Management solution.
 - b. Tynarace Solutions
 - c. Health Monitoring and Energy monitoring
3. Research Work
 - a. Simulation environment setup in MATLAB
 - b. Create a working node-to-node connection in LoRaWAN
 - c. Implement the working LoRaWAN network with at least 6 nodes
 - d. Simulate improvements in the authentication mechanism
5. Coordinate with operations team on repetitive problems arising in the tickets, engage in troubleshooting calls.

Employer comments

Adil always takes an initiative to learn. He has a positive attitude towards any task assigned to him and engages himself in KT sessions to enhance his learning. The research and Operations team were happy with his performance.

Student Signature: _____  _____ Date: 30/11/2023

Industry Supervisor Signature: _____  _____ Date: 30/11/2023